Title (en)

PIXEL ADDRESSING CIRCUIT AND METHOD OF CONTROLLING SUCH CIRCUIT

Title (de)

PIXELADRESSIERUNGSSCHALTUNG UND VERFAHREN ZUR STEUERUNG SOLCHER SCHALTUNG

Title (fr)

CIRCUIT D'ADRESSAGE DE PIXELS ET PROCEDE DE CONTROLE D'UN TEL CIRCUIT

Publication

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Application

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Abstract (en)

[origin: WO2006087477A1] The invention relates to a pixel addressing circuit and to a method of controlling one such circuit. The pixel addressing circuit (1) comprises two actuating transistors (T1a, T1b) which are connected in series with one diode (D) to the terminals of a supply voltage (Vcc) and two switching transistors (T2a, T2b) which each comprise a grid and which are connected respectively between data signals (Vda, Vdb) and the grid of the associated actuating transistors (T1a, T1b). The grids of the switching transistors (T2a, T2b) are connected to two different outputs of a control circuit (2) supplying same with different addressing voltages (Vg2a, Vg2b). The method of controlling the addressing circuit (1) consists in supplying the grids of the switching transistors (T2a, T2b) with addressing voltages (Vg2a, Vg2b) that can respectively render the associated actuating transistors (T1a, T1b) blocked and conducting, such as to change one of the actuating transistors (T1a, T1b) to a diode (D) control and addressing phase and the other actuating transistor (T1a, T1b) to a repair phase.

IPC 8 full level

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