

Title (en)

Method and computer program product for running a program on a processor with multi-threading architecture

Title (de)

Verfahren und Computerprogrammprodukt zur Ausführung eines Programms auf einem Prozessor mit Multithreading-Architektur

Title (fr)

Procédé et produit de programme informatique destinés à l'exécution d'un programme sur un processeur à architecture multithread

Publication

EP 1855192 A3 20081210 (DE)

Application

EP 07006192 A 20070326

Priority

DE 102006020178 A 20060502

Abstract (en)

[origin: EP1855192A2] The method involves identifying two processes of the program, which are executable independently, parallel to each other and use common resources. Two identified processes are assigned to different threads of the processor. The program is processed by parallel processing of two identified processes in the assigned threads. An independent claim is also included for a computer program product with program code for processing a computer program on the computer.

IPC 8 full level

G06F 9/38 (2006.01)

CPC (source: EP US)

G06F 9/3851 (2013.01 - EP US)

Citation (search report)

- [XY] US 5724586 A 19980303 - EDLER JAN STERLING [US], et al
- [X] US 6938252 B2 20050830 - BAYLOR SANDRA JOHNSON [US], et al
- [X] US 5974438 A 19991026 - NEUFELD E DAVID [US]
- [Y] AGESEN O ET AL: "MIXED-MODE BYTECODE EXECUTION", SUN MICROSYSTEMS LAB. TECH. REPORT, XX, XX, 1 June 2000 (2000-06-01), pages 1 - 13, XP001159778
- [A] ZHENG WEIMIN ET AL: "A java virtual machine design based on hybrid concurrent compilation model", TECHNOLOGY OF OBJECT-ORIENTED LANGUAGES AND SYSTEMS, 2000. TOOLS - ASI A 2000. PROCEEDINGS. 36TH INTERNATIONAL CONFERENCE ON OCT. 30 - NOV. 4, 2000, PISCATAWAY, NJ, USA, IEEE, 30 October 2000 (2000-10-30), pages 18 - 23, XP010524216, ISBN: 978-0-7695-0875-7
- [A] RADHAKRISHNAN R ET AL: "JAVA RUNTIME SYSTEMS: CHARACTERIZATION AND ARCHITECTURAL IMPLICATIONS", IEEE TRANSACTIONS ON COMPUTERS, IEEE SERVICE CENTER, LOS ALAMITOS, CA, US, vol. 50, no. 2, 1 February 2001 (2001-02-01), pages 131 - 146, XP008060381, ISSN: 0018-9340
- [A] KRINTZ C J ET AL: "REDUCING THE OVERHEAD OF DYNAMIC COMPILATION", SOFTWARE PRACTICE & EXPERIENCE, WILEY & SONS, BOGNOR REGIS, GB, vol. 31, no. 8, 10 July 2001 (2001-07-10), pages 717 - 738, XP001065393, ISSN: 0038-0644
- [A] DEBBABI ET AL: "A selective dynamic compiler for embedded Java virtual machines targeting ARM processors", SCIENCE OF COMPUTER PROGRAMMING, ELSEVIER SCIENCE PUBLISHERS BV., AMSTERDAM, NL, vol. 59, no. 1-2, 1 January 2006 (2006-01-01), pages 38 - 63, XP005157386, ISSN: 0167-6423

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

Designated extension state (EPC)

AL BA HR MK RS

DOCDB simple family (publication)

EP 1855192 A2 20071114; EP 1855192 A3 20081210; DE 102006020178 A1 20071108; US 2007266224 A1 20071115

DOCDB simple family (application)

EP 07006192 A 20070326; DE 102006020178 A 20060502; US 74343007 A 20070502