

Title (en)

METHOD AND APPARATUS FOR DESIGNING A PLL

Title (de)

VERFAHREN UND VORRICHTUNG ZUR KONSTRUKTION EINES PHASENREGELKREISES

Title (fr)

METHODE ET APPAREIL POUR CONCEVOIR UN PLL

Publication

EP 1875609 A4 20100728 (EN)

Application

EP 06741404 A 20060421

Priority

- CA 2006000639 W 20060421
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Abstract (en)

[origin: WO2006113986A1] A method and apparatus for designing a PLL enables initial component characteristics and design specifications of the PLL to be specified. Time constants for a loop filter that would be required to create a PLL having the desired design specifications and component characteristics are then computed. The performance or behavior characteristics of the PLL may then be computed for the PLL given the time constants and the initial set of components, to determine whether the performance of the PLL would be considered satisfactory. For example, PLL design software may determine whether a PLL would be sufficiently stable if it was to be created using the particular selected components given the required design specifications. Where the PLL does not meet particular behavior characteristics, the PLL design software may provide guidance as to what component characteristics would improve performance of the PLL. Designed PLLs may be used for timestamp based clock synchronization.

IPC 8 full level

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CPC (source: EP KR)

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Citation (search report)

- [XY] US 6697445 B1 20040224 - WONG HEE [US]
- [Y] US 5864248 A 19990126 - ROKUGO YOSHINORI [JP]
- [A] EP 0924862 A2 19990623 - NEC CORP [JP]
- See references of WO 2006113986A1

Designated contracting state (EPC)

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