

Title (en)
INTEGRATED CIRCUIT WITH WARPING CALCULATION UNIT

Title (de)
INTEGRIERTE SCHALTUNG MIT WARPING-BERECHNUNGSEINHEIT

Title (fr)
CIRCUIT INTEGRE POURVU D'UNE UNITE DE CALCUL DE DEFORMATION

Publication
EP 1886274 A1 20080213 (DE)

Application
EP 06763365 A 20060530

Priority

- EP 2006062712 W 20060530
- DE 102005025025 A 20050530

Abstract (en)
[origin: WO2006128862A1] The invention relates to an integrated circuit for the electronic manipulation of images, a method for the electronic manipulation of images by means of warping, a system for the electronic manipulation of images comprising an integrated circuit, and also a use of integrated circuit according to the invention. The object of providing an integrated circuit for the electronic manipulation of images and also a system for the electronic manipulation of images comprising an integrated circuit, which integrated circuit and which system ensure a high computing power with regard to a dynamic warping of real-time images and at the same enable low unit costs for the hardware, is achieved by virtue of the fact that the integrated circuit has a main processor unit (3), a memory access control unit (9), a graphics output control unit (5) and a warping calculation unit (6).

IPC 8 full level
G06T 3/00 (2006.01)

CPC (source: EP)
G06T 3/18 (2024.01); **G06T 2200/28** (2013.01)

Designated contracting state (EPC)
DE FR IT

DOCDB simple family (publication)
DE 102005025025 A1 20061207; DE 202005021565 U1 20080911; EP 1886274 A1 20080213; JP 2008543071 A 20081127; WO 2006128862 A1 20061207

DOCDB simple family (application)
DE 102005025025 A 20050530; DE 202005021565 U 20050530; EP 06763365 A 20060530; EP 2006062712 W 20060530; JP 2008514089 A 20060530