

Title (en)  
**MEMORY CONTROLLER**

Title (de)  
**SPEICHERSTEUERUNG**

Title (fr)  
**CONTROLEUR DE MEMOIRE**

Publication  
**EP 1894108 A2 20080305 (EN)**

Application  
**EP 06765728 A 20060613**

Priority  

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Abstract (en)  
[origin: WO2006134550A2] A memory controller (SMC) is provided for coupling a memory (MEM) to a network (N). The memory controller (SMC) comprises a first interface (PI), a streaming memory unit (SMU) and a second interface. The first interface (PI) is used for connecting the memory controller (SMC) to the network (N) for receiving and transmitting data streams (STI - ST4). The streaming memory unit (SMU) is coupled to the first interface (PI) for controlling data streams (STI - ST4) between the network (N) and the memory (MEM). The streaming memory unit (SMU) comprises a buffer (B) for temporarily storing at least part of the data stream (STI - ST4) and a buffer managing unit (BMU) for managing the temporarily storing of the data streams (STI - ST4) in the buffer (B) and for dynamically allocating buffers (PFB, WBB) for at least one of the data streams (STI - ST4). The second interlace is coupled to the streaming memory unit (SMU) for connecting the memory controller (SMC) to the memory (MEM) in order to exchange data with the memory (MEM) in bursts. Furthermore, a buffer dimensioning unit (BDU) is provided for dimensioning the buffer (B) for at least one of the data streams (STI - ST4).

IPC 8 full level  
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CPC (source: EP)  
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