Title (en)

SYSTEM AND METHOD FOR PROVIDING OPTIMIZED RECEIVER ARCHITECTURES FOR COMBINED PILOT AND DATA SIGNAL TRACKING

Title (de)

SYSTEM UND VERFAHREN ZUR BEREITSTELLUNG VON OPTIMIERTEN EMPFÄNGERARCHITEKTUREN ZUM KOMBINIERTEN VERFOLGEN VON PILOT- UND DATENSIGNALEN

Title (fr)

SYSTEME ET PROCEDE PERMETTANT OBTENIR DES ARCHITECTURES DE RECEPTEUR OPTIMISEES POUR LA POURSUITE COMBINEE DE SIGNAUX DE DONNEES ET DE SIGNAUX PILOTES

Publication

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Application

EP 06795060 A 20060628

Priority

- IB 2006001793 W 20060628
- US 17087605 A 20050630

Abstract (en)

[origin: WO2007004017A1] A system architecture for a receiver to process multiple signals on a common carrier frequency from a satellite. The receiver is arranged such that the receiver receives input data transmitted from the satellite. A pilot signal is tracked from the input data using a correlation channel, and a data signal is tracked from the input data using a data code generator operatively connected to the correlation channel. In one embodiment of the invention, the data signal generator creates replica code for the data signal. In another embodiment of the invention, the system can switch between the data signal generator and pilot signal generator based upon the signal-to-noise ratio of the incoming signal.

IPC 8 full level

G01S 1/00 (2006.01); **G01S 19/24** (2010.01); **G01S 19/32** (2010.01); **G01S 19/35** (2010.01); **H04B 1/7085** (2011.01)

CPC (source: EP US)

G01S 19/24 (2013.01 - EP US); **G01S 19/32** (2013.01 - EP US); **G01S 19/35** (2013.01 - EP US); **H04B 1/7085** (2013.01 - EP US); H04B 2201/70701 (2013.01 - EP US); H04B 2201/70715 (2013.01 - EP US)

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