

Title (en)

Method for manufacturing a thin film transistor array panel

Title (de)

Verfahren zur Herstellung einer Anzeigevorrichtung auf Basis von Dünnschichttransistoren

Title (fr)

Procédé de fabrication d'un panneau à réseau de transistors à couche mince

Publication

**EP 1903609 A2 20080326 (EN)**

Application

**EP 07018544 A 20070921**

Priority

KR 20060092228 A 20060922

Abstract (en)

A method for manufacturing a thin film transistor ("TFT") array panel includes forming a gate electrode, forming source and drain electrodes insulated from the gate electrode, forming an organic semiconductor contacting the source and drain electrodes, spraying a solvent on the organic semiconductor, drying the solvent at room temperature, and annealing the organic semiconductor.

IPC 8 full level

**H01L 27/28** (2006.01); **H01L 51/05** (2006.01)

CPC (source: EP KR US)

**H10K 10/466** (2023.02 - EP KR US); **H10K 10/484** (2023.02 - KR); **H10K 71/135** (2023.02 - KR); **H10K 71/441** (2023.02 - KR); **H10K 10/484** (2023.02 - EP US); **H10K 71/135** (2023.02 - EP US); **H10K 71/441** (2023.02 - EP US)

Cited by

RU2698739C2; RU2731839C2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

Designated extension state (EPC)

AL BA HR MK YU

DOCDB simple family (publication)

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