

Title (en)
SYNCHRONIZED HIGH-ASSURANCE CIRCUITS

Title (de)
SYNCHRONISIERTE HOCH-SICHERSTELLUNGS-SCHALTUNGEN

Title (fr)
CIRCUITS A ASSURANCE ELEVEE SYNCHRONISES

Publication
EP 1907937 A4 20100113 (EN)

Application
EP 06786509 A 20060705

Priority

- US 2006026376 W 20060705
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- US 69707205 P 20050705
- US 42850806 A 20060703
- US 42851606 A 20060703

Abstract (en)
[origin: WO2007006013A2] A high-assurance system for processing information is disclosed. The high-assurance system comprising first and second processors, a task matching circuit, and first and second outputs. The task matching circuit configured to determine a software routine is ready for execution on the first processor, and delay the first processor until the second processor is ready to execute the software routine. The first output of the first processor configured to produce a first result with the software routine. The second output of the second processor configured to produce a second result with the software routine, where the first result is identical to the second result.

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CPC (source: EP)
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Citation (search report)

- [X] US 6363464 B1 20020326 - MANGIONE MICHAEL T [US]
- [X] EP 0674262 A1 19950927 - IBM [US]
- See references of WO 2007006013A2

Citation (examination)

- GB 2399426 A 20040915 - SHARP KK [JP]
- US 5845060 A 19981201 - VRBA RICHARD ALAN [US], et al

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