Title (en)

RECONCILIATION MECHANISM USING RFID AND SENSORS

Title (de)

ABSTIMMECHANISMUS MIT RFID UND SENSOREN

Title (fr)

MECANISME DE RECONCILIATION UTILISANT DES IDENTIFICATIONS RADIOFREQUENCE ET DES CAPTEURS

Publication

EP 1910982 A1 20080416 (EN)

Application

EP 05776159 A 20050719

Priority

EP 2005007878 W 20050719

Abstract (en)

[origin: WO2007009475A1] An RFID ASIC (Application-Specific Integrated Circuit) that can receive and store digital data from at least one of sensors and A/D converter devices. The ASIC can include one or more of the following components as desired for a given application: a passive power/communications coupling component with antenna that couples power into the ASIC when employed as a passive device, and for facilitating communications with the device when powered; a control module that control or facilitates the control of all onboard functions; a memory for storing data (e.g., timestamp and event information) and/or programs; a clock for the generation of clock signals and in support of onboard timing requirement; and, a digital I/O interface that facilitates interfacing to digital inputs such as from sensors and/or A/D converters.

IPC 8 full level

G06K 19/077 (2006.01)

CPC (source: EP)

G06K 19/0716 (2013.01); **G06K 19/077** (2013.01)

Citation (search report)

See references of WO 2007009475A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

WO 2007009475 A1 20070125; EP 1910982 A1 20080416

DOCDB simple family (application)

EP 2005007878 W 20050719; EP 05776159 A 20050719