

Title (en)

ARC FAULT DETECTION AND CONFIRMATION USING VOLTAGE AND CURRENT ANALYSIS

Title (de)

LICHTBOGENFEHLERDETEKTION UND BESTÄTIGUNG MITTELS SPANNUNGS- UND STROMANALYSE

Title (fr)

DETECTION ET CONFIRMATION D'UNE DEFAILLANCE D'ARC AU MOYEN D'ANALYSES DE TENSION ET DE COURANT

Publication

**EP 1924861 A2 20080528 (EN)**

Application

**EP 06803684 A 20060915**

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Abstract (en)

[origin: WO2007035488A2] An arc fault in an electrical circuit is detected by recording phase plane plot data of current versus voltage for at least one electrical cycle of an alternating current signal, and applying a phase plane plot arc fault monitoring algorithm to the phase plane plot data to determine whether an arc fault is present. An arc fault detection algorithm and an arc fault confirmation algorithm are employed to detect and confirm the presence of series and parallel arc faults in a circuit.

IPC 8 full level

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CPC (source: EP)

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