Title (en)

MEMORY ARRANGEMENT AND METHOD FOR THE OPERATION THEREOF

Title (de)

SPEICHERANORDNUNG UND BETRIEBSVERFAHREN DAFÜR

Title (fr)

SYSTEME DE MEMOIRE ET SON MODE DE FONCTIONNEMENT

Publication

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Application

EP 06778041 A 20060728

Priority

• EP 2006064768 W 20060728
• DE 102005040916 A 20050830

Abstract (en)

[origin: WO2007025816A2] The invention relates to a memory arrangement which comprises a writable data memory (102) and means for recognising (103) an error in a data word which is read by the data memory (102), and which is used to correct (101) the error and to memorise (101) the corrected data word on a new address in a free area of the data memory (102).

IPC 8 full level

G06F 11/10 (2006.01)

CPC (source: EP KR US)

G06F 11/08 (2013.01 - KR); **G06F 11/1008** (2013.01 - EP US); **G06F 12/16** (2013.01 - KR); **G11C 29/00** (2013.01 - KR)

Citation (search report)

See references of WO 2007025816A2

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