

Title (en)
DATA PROCESSING ARRANGEMENT AND THE OPERATION MODE THEREOF

Title (de)
DATENVERARBEITUNGSANLAGE UND VERFAHREN ZUM BETRIEB

Title (fr)
INSTALLATION DE TRAITEMENT DE DONNEES ET SON MODE DE FONCTIONNEMENT

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Abstract (en)
[origin: WO2007033923A1] The invention relates to a data processing arrangement, in particular to a tachograph (DTCO) for a motor vehicle comprising a control device (CPU) and at least one memory (MEM). The aim of said invention is to better guarantee the traceability of an error arising during the data processing arrangement operation. According to said invention, at least one partial area of the memory (MEM) is embodied in the form of a sequence memory (SEQ) and a control device (CPU) is constructed in such a way that sequences of the data processing arrangement or of the tachograph (DTCO) are recorded in the sequence memory (SEQ), wherein the sequence memory (SEQ) is provided with at least one ring memory (RNG) which is organised in such a way an oldest input (OER) is overwritten by a new input (NER), respectively. Said invention makes it possible to record a plurality of sequential events leading to a substantial error in the sequence memory (SEQ) in such a way the size of the memory (MEM) is increased within economically acceptable limits during a long operation of the arrangement.

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