

Title (en)

STROBE TECHNIQUE FOR TIME STAMPING A DIGITAL SIGNAL

Title (de)

STROBE-TECHNIK ZUR ZEITSTEMPELUNG EINES DIGITALEN SIGNALS

Title (fr)

TECHNIQUE STROBOSCOPIQUE D'HORODATAGE D'UN SIGNAL NUMERIQUE

Publication

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Application

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Abstract (en)

[origin: WO2007038233A2] A test system timing method simulates the timing of a synchronous clock on the device under test. Strobe pulses can be generated by routing an edge generator to delay elements with incrementally increasing delay values. A data signal or synchronous clock signal can be applied to the input of each of a set of latches which are clocked by the strobe pulses. An encoder can convert the series of samples which are thereby latched to a word representing edge time and polarity of the sampled signal. If the sampled signal is a data signal, the word can be stored in memory. If the sampled signal is a clock signal, the word is routed to a clock bus and used to address the memory. The difference between clock edge time and data edge time is provided and can be compared against expected values.

IPC 8 full level

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