

Title (en)

Blocking device to prevent misplacements in printed circuit board connectors

Title (de)

Blockiervorrichtung gegen Fehlsteckungen bei Leiterplattensteckverbindern

Title (fr)

Dispositif de blocage contre les mauvais enfichages pour connecteurs enfichables de cartes de circuits imprimés

Publication

EP 1930992 B1 20111005 (DE)

Application

EP 07023314 A 20071201

Priority

DE 202006018590 U 20061208

Abstract (en)

[origin: US2008139010A1] In order to prevent the mismatching of several daughterboards (2) that are combined into a plug-in unit (20) with corresponding board connectors (3) on a motherboard (1), it is proposed that the mating of the plug-in unit (20) is effectively prevented by means of a blocking block (10) that is inserted into an intermediate space between two board connectors (3) that are spaced apart from one another by a regular connector spacing and snapped into window-like recesses (4) of the board connectors (3).

IPC 8 full level

H01R 12/73 (2011.01); **H01R 13/64** (2006.01)

CPC (source: EP US)

H01R 12/7005 (2013.01 - EP US); **H01R 12/716** (2013.01 - EP US); **H01R 13/6456** (2013.01 - EP US); **H01R 12/7082** (2013.01 - EP US)

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

US 2008139010 A1 20080612; **US 7503766 B2 20090317**; AT E527724 T1 20111015; CN 101197477 A 20080611; CN 101197477 B 20100929; DE 202006018590 U1 20070215; DK 1930992 T3 20120123; EP 1930992 A2 20080611; EP 1930992 A3 20091216; EP 1930992 B1 20111005; ES 2372764 T3 20120126; JP 2008147192 A 20080626; JP 4700674 B2 20110615; PL 1930992 T3 20120330

DOCDB simple family (application)

US 94586307 A 20071127; AT 07023314 T 20071201; CN 200710198918 A 20071207; DE 202006018590 U 20061208; DK 07023314 T 20071201; EP 07023314 A 20071201; ES 07023314 T 20071201; JP 2007316168 A 20071206; PL 07023314 T 20071201