

Title (en)

HARDWARE DRIVER INTEGRITY CHECK OF MEMORY CARD CONTROLLER FIRMWARE

Title (de)

HARDWARE-TREIBERINTEGRITÄTSPRÜFUNG VON SPEICHERKARTEN-CONTROLLER-FIRMWARE

Title (fr)

VERIFICATION D'INTEGRITE DU PILOTE D'UN MICROLOGICIEL DE CONTROLEUR DE CARTE MEMOIRE

Publication

**EP 1934880 A2 20080625 (EN)**

Application

**EP 06814655 A 20060913**

Priority

- US 2006035840 W 20060913
- US 71734705 P 20050914
- US 28462305 A 20051121
- US 28560005 A 20051121

Abstract (en)

[origin: WO2007033322A2] A memory system comprises an encryption engine implemented in the hardware of a controller. In starting up the memory system, a boot strapping mechanism is implemented wherein a first portion of firmware when executed pulls in another portion of firmware to be executed. The hardware of the encryption engine is used to verify the integrity of at least the first portion of the firmware. Therefore, only the firmware that is intended to run the system will be executed.

IPC 8 full level

**G06F 21/57** (2013.01)

CPC (source: EP KR)

**G06F 12/00** (2013.01 - KR); **G06F 21/00** (2013.01 - KR); **G06F 21/57** (2013.01 - EP)

Citation (search report)

See references of WO 2007033322A2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

**WO 2007033322 A2 20070322; WO 2007033322 A3 20070510**; CN 102142070 A 20110803; CN 102142070 B 20131106;  
EP 1934880 A2 20080625; EP 2320345 A2 20110511; EP 2320345 A3 20110810; JP 2009508272 A 20090226; JP 2011210278 A 20111020;  
JP 5089593 B2 20121205; JP 5411896 B2 20140212; KR 100973733 B1 20100804; KR 20080045708 A 20080523

DOCDB simple family (application)

**US 2006035840 W 20060913**; CN 201110059311 A 20060913; EP 06814655 A 20060913; EP 11001172 A 20060913;  
JP 2008531324 A 20060913; JP 2011137416 A 20110621; KR 20087006251 A 20060913