

Title (en)

CIRCUIT STATE SCAN-CHAIN, DATA COLLECTION SYSTEM AND EMULATION METHOD

Title (de)

SCHALTUNGSZUSTANDS-SCAN-KETTE, DATENSAMMELSYSTEM UND EMULATIONSVERFAHREN

Title (fr)

CHAÎNE DE BALAYAGE DE L'ÉTAT DU CIRCUIT, SYSTÈME DE COLLECTE DE DONNÉES ET PROCÉDÉ D'ÉMULATION

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Application

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Abstract (en)

The present invention provides a circuit state scan-chain for emulating and verifying integrated circuit design, a data collection system and an emulation and verification method using the scan-chain. The said integrated circuit includes a number of registers and the corresponding input terminal combinational logics and output terminal combinational logics. The construction of the said scan-chain includes the first multiplex module and the second multiplex module arranged with regard to each register, changing the operation mode of the said integrated circuit by controlling the first multiplex module and the second multiplex module, enabling the said integrated circuit to switch among the normal mode, holding mode and snapshot mode, and enabling the registers to form a scan-chain loop in the snapshot mode.

IPC 8 full level

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