

Title (en)

APPARATUS AND METHOD FOR ELIMINATING ERRORS IN A SYSTEM HAVING AT LEAST TWO EXECUTION UNITS WITH REGISTERS

Title (de)

VORRICHTUNG UND VERFAHREN ZUM BEHEBEN VON FEHLERN BEI EINEM WENIGSTENS ZWEI AUSFÜHRUNGSEINHEITEN MIT REGISTERN AUFWEISENDEN SYSTEM

Title (fr)

DISPOSITIF ET PROCÉDÉ D'ÉLIMINATION DE DÉFAUTS DANS UN SYSTÈME PRÉSENTANT AU MOINS DEUX UNITÉS D'EXÉCUTION AVEC REGISTRES

Publication

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Application

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Priority

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Abstract (en)

[origin: WO2007057271A1] An apparatus (120) for eliminating errors in a system (100, 400) having at least two execution units (101, 102) with registers is presented, wherein the registers are designed to hold data. The apparatus has comparison means (126) which are set up in such a manner that a discrepancy and thus an error can be determined by comparing data which are intended to be stored in the registers. At least one shadow register (121, 122), which is set up in such a manner that it can store data relating to data from the registers, and means for restoring error-free data in at least one register on the basis of the data in the at least one shadow register (121, 122) in the event of an error being determined are furthermore provided. This apparatus can be used to improve the reliability of a multi-core processor (100).

IPC 8 full level

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CPC (source: EP KR US)

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