

Title (en)

Ceramic material used for protection against electrical overstress and low-capacitance multilayer chip varistor using the same

Title (de)

Keramikmaterial zur Verwendung als Schutz gegen elektrische Überspannung und Mehrschicht-Chip-Varistor mit niedriger Kapazität umfassend dieses Keramikmaterial

Title (fr)

Matériau céramique utilisé pour la protection contre les surcharges électriques et varistance à puce multicouche à faible capacité l'utilisant

Publication

EP 1956612 B1 20141231 (EN)

Application

EP 07102045 A 20070209

Priority

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Abstract (en)

[origin: EP1956612A1] A low-capacitance multilayer chip varistor (10) has capacitance lower than 0.5pF at 1MHz and has a characteristic of resisting more than thousands of times of 8KV electrostatic shock, which comprises a ceramic main body (11), outer electrodes (13) disposed at two ends of the ceramic main body (11) and inner electrodes (12) disposed therein; the ceramic main body (11) comprises inorganic glass of 3~50wt% and semi-conductive or conductive particles (14) of 50~97wt% with particle size of more than 0.1¼m, and a layer of inorganic glass film (15) covers the surface of semi-conductive or conductive particles (14), wherein the inorganic glass film (15) contains semi-conductive or conductive particles (16) of submicron or nanometer which is smaller than 1 micron., and the quantity contained of semi-conductive or conductive particles (16) is less than 20wt% of that of inorganic glass.

IPC 8 full level

H01C 7/10 (2006.01)

CPC (source: EP)

H01C 7/105 (2013.01); **H01C 7/108** (2013.01); **H01C 7/115** (2013.01); **H01C 7/118** (2013.01)

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