

Title (en)

OPERATING SYSTEM FOR A CHIP CARD COMPRISING A MULTI-TASKING KERNEL

Title (de)

BETRIEBSSYSTEM FÜR EINE CHIPKARTE MIT EINEM MULTI - TASKING KERNEL

Title (fr)

SYSTÈME D'EXPLOITATION POUR UNE CARTE À PUCE COMPRENANT UN NOYAU À FONCTIONNEMENT MULTITÂCHE

Publication

**EP 1989621 A1 20081112 (DE)**

Application

**EP 07722887 A 20070221**

Priority

- EP 2007001511 W 20070221
- DE 102006008248 A 20060222

Abstract (en)

[origin: DE102006008248A1] The method involves implementing different applications (A1, A2) e.g. services, on a mobile data carrier (C) e.g. chip card, at the same time, and operating the mobile data carrier by a central control unit (MTK). The operation of the mobile data carrier is controlled and/or monitored by the central control unit in such a manner that each application is allotted in accordance to a scheduling mechanism resource, and/or a data exchange between a microprocessor and a data storage is controlled. Independent claims are also included for the following: (1) a mobile data carrier comprising a microprocessor (2) a computer program product loadable directly into a data storage of a programmable mobile data carrier or in a control device, comprising program code medium for implementing a method for operating a mobile data carrier (3) a method for producing and/or for holding a mobile data carrier.

IPC 8 full level

**G06F 9/48** (2006.01)

CPC (source: EP US)

**G06F 9/4881** (2013.01 - EP US)

Citation (search report)

See references of WO 2007096153A1

Citation (examination)

TANENBAUM A S: "Modern Operating Systems, PROCESSES AD THREADS", MODERN OPERATING SYSTEMS, XX, XX, 1 January 2001 (2001-01-01), pages 71 - 73,81, XP002372417

Cited by

CN106657582A

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