

Title (en)
 Pipelining processes in a RF reader

Title (de)
 Fließband-Verfahren in einem RF-Leser

Title (fr)
 Processus de pipe-line dans un lecteur RF

Publication
 EP 1995902 A2 20081126 (EN)

Application
 EP 08156683 A 20080521

Priority
 US 75334607 A 20070524

Abstract (en)
 The present disclosure includes a method and system for pipelining processes in an RF reader. In some implementations, a method includes receiving, from a transponder, a reply to an initial command. A physical (PHY) layer and a media control access (MAC) layer are pipelined to initiate transmission of a subsequent command during demodulation of the transponder reply.

IPC 8 full level
 G06K 19/00 (2006.01); **H04B 17/00** (2006.01); **H04L 1/00** (2006.01)

CPC (source: EP US)
 H04L 1/0052 (2013.01 - EP US); **H04L 1/0061** (2013.01 - EP US)

Citation (applicant)
 N. ROY ET AL.: "Designing an FPGA-Based RFID Reader", XCELL JOURNAL, 2006, pages 26 - 28

Designated contracting state (EPC)
 AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT RO SE SI SK TR

Designated extension state (EPC)
 AL BA MK RS

DOCDB simple family (publication)
 EP 1995902 A2 20081126; **EP 1995902 A3 20130403**; **EP 1995902 B1 20140702**; ES 2500216 T3 20140930; US 2008290996 A1 20081127; US 8248212 B2 20120821

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 EP 08156683 A 20080521; ES 08156683 T 20080521; US 75334607 A 20070524