

Title (en)

Analogue divider

Title (de)

Analog Dividierer

Title (fr)

Diviseur analogique

Publication

EP 2012250 B1 20131113 (DE)

Application

EP 08103649 A 20080422

Priority

AT 9432007 A 20070619

Abstract (en)

[origin: EP2012250A2] The method involves comparing a saw tooth or triangle signal with an input voltage (U2) by a comparator (KO1) so that a pulse-width-modulated signal is generated as a comparison signal (SIG1out). An input voltage (U1) of the triangle signal or a voltage proportional to the voltage (U1) and the signal are supplied to a controller (REG1) such that an upper peak value of the triangle signal is readjusted. A reference potential and the triangle signal are supplied to another controller (REG2) such that a lower peak value of the triangle signal is readjusted to a value of the potential. An independent claim is also included for an analog divider having a saw tooth or a triangle signal generator.

IPC 8 full level

G06G 7/16 (2006.01); **G06G 7/161** (2006.01)

CPC (source: EP)

G06G 7/16 (2013.01); **G06G 7/161** (2013.01)

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT RO SE SI SK TR

DOCDB simple family (publication)

EP 2012250 A2 20090107; **EP 2012250 A3 20111012**; **EP 2012250 B1 20131113**; AT 505446 A1 20090115; AT 505446 B1 20090815

DOCDB simple family (application)

EP 08103649 A 20080422; AT 9432007 A 20070619