

Title (en)

WRITING TO AND CONFIGURING FLASH MEMORY

Title (de)

BESCHREIBEN UND EINSTELLEN VON FLASH-SPEICHERN

Title (fr)

ÉCRITURE DANS UNE MÉMOIRE FLASH ET CONFIGURATION DE CETTE DERNIÈRE

Publication

EP 2016494 A4 20100203 (EN)

Application

EP 07756993 A 20070214

Priority

- US 2007062143 W 20070214
- US 35387306 A 20060214
- US 35387406 A 20060214

Abstract (en)

[origin: WO2007095579A2] Writing to flash memory may include receiving write commands that are substantially independent of an internal architecture of the flash memory. In certain implementations, two operand flash commands are received at a flash controller from a remote controller, and the writing process may further include translating each two-operand write command into architecture-dependent flash commands; executing the architecture-dependent flash commands to fill a page buffer associated with the flash memory; and subsequently transferring contents of the page buffer to a page of flash memory. A system for configuring or testing memory may cycle a memory array while substantially concurrently performing other functional testing. In particular implementations, the system may configure, or cycle, a flash memory (e.g., by erasing and writing specific patterns to dissipate charge that accumulated during a fabrication process) using a serial interface and test other functional units using the same serial interface substantially concurrently.

IPC 8 full level

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CPC (source: EP)

G11C 29/08 (2013.01); **G11C 16/04** (2013.01); **G11C 2029/0401** (2013.01)

Citation (search report)

- [X] US 2005195635 A1 20050908 - CONLEY KEVIN M [US], et al
- [X] US 2005268025 A1 20051201 - SMITH PETER [GB], et al
- [XP] WO 2006068916 A1 20060629 - SANDISK CORP [US], et al
- See references of WO 2007095579A2

Designated contracting state (EPC)

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DOCDB simple family (publication)

WO 2007095579 A2 20070823; **WO 2007095579 A3 20081204**; EP 2016494 A2 20090121; EP 2016494 A4 20100203

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