

Title (en)  
PROCESSING DEVICE

Title (de)  
VERARBEITUNGSEINRICHTUNG

Title (fr)  
DISPOSITIF DE TRAITEMENT

Publication  
**EP 2023292 A1 20090211 (EN)**

Application  
**EP 07743133 A 20070511**

Priority  
• JP 2007059698 W 20070511  
• JP 2006132164 A 20060511

Abstract (en)  
A processing device for performing a geometry process performed as preprocessing for rendering a three-dimensional object on a display by modeling the three-dimensional object using a polygon mesh. The geometry process includes a vertex process that is performed for each of the vertices of the polygon mesh by a different one of a plurality of processors, and processed vertex data obtained by the vertex process is notified among the processors so that a polygon process can be performed in each of the processors. Since each processor can continuously perform the polygon process immediately after the vertex process, it is possible to suppress the occurrence of the unbalance of timing in performing the vertex process and the polygon process, thereby efficiently performing computation while minimizing the wasteful idle time of the processors.

IPC 8 full level  
**G06T 1/20** (2006.01); **G06T 15/00** (2011.01)

CPC (source: EP US)  
**G06T 15/005** (2013.01 - EP US)

Cited by  
EP2984564A4; GB2593518A; GB2593518B; US10126903B2; US11295482B2; US9760964B2; US10354363B2; US10896484B2

Designated contracting state (EPC)  
DE FR GB

Designated extension state (EPC)  
AL BA HR MK RS

DOCDB simple family (publication)  
**EP 2023292 A1 20090211**; **EP 2023292 A4 20161019**; **EP 2023292 B1 20200226**; JP 2011175656 A 20110908; JP 4759614 B2 20110831; JP 5020393 B2 20120905; JP WO2007132746 A1 20090924; US 2009207169 A1 20090820; US 2012218265 A1 20120830; US 8180182 B2 20120515; US 8331734 B2 20121211; WO 2007132746 A1 20071122

DOCDB simple family (application)  
**EP 07743133 A 20070511**; JP 2007059698 W 20070511; JP 2008515521 A 20070511; JP 2011082455 A 20110404; US 201213443184 A 20120410; US 29951807 A 20070511