

Title (en)  
MEMORIES WITH FRONT END PRECHARGE

Title (de)  
SPEICHEREINHEITEN MIT FRONT-END-VORLADUNG

Title (fr)  
MÉMOIRES AVEC PRÉCHARGE FRONTALE

Publication  
**EP 2041750 B1 20100825 (EN)**

Application  
**EP 07812687 A 20070706**

Priority  
• US 2007072974 W 20070706  
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• US 77185307 A 20070629

Abstract (en)  
[origin: WO2008006075A3] Methods, apparatuses and systems (2900) of operating digital memory (2904) where the digital memory device (2904) including a plurality of memory cells (713) receives a command to perform an operation on a set of memory cells (713), where the set of memory cells (713) contains fewer memory cells than the device (2904) as a whole and where the device (2904) performs the operation including selectively precharging on the front end of the operation, in response to the receive command, only a set of bit lines (709) associated with the set of memory cells (713).

IPC 8 full level  
**G11C 7/12** (2006.01); **G11C 7/00** (2006.01); **G11C 7/06** (2006.01)

CPC (source: EP KR)  
**G11C 5/005** (2013.01 - EP); **G11C 5/14** (2013.01 - KR); **G11C 7/12** (2013.01 - EP KR); **G11C 7/18** (2013.01 - EP); **G11C 11/405** (2013.01 - EP); **G11C 11/4094** (2013.01 - EP); **G11C 11/4097** (2013.01 - EP)

Cited by  
EP3217406A4; US10127955B2

Designated contracting state (EPC)  
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

DOCDB simple family (publication)  
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