

Title (en)
BOOSTER CIRCUIT

Title (de)
VERSTÄRKUNGSSCHALTUNG

Title (fr)
CIRCUIT AMPLIFICATEUR

Publication
EP 2052537 A1 20090429 (EN)

Application
EP 07808287 A 20070918

Priority
• KR 2007004497 W 20070918
• KR 20060092262 A 20060922

Abstract (en)
[origin: WO2008035893A1] A booster circuit comprises a rejection filter for filtering a signal, which has a frequency band higher than a preset frequency band, among input signals, and a booster for receiving and amplifying a signal output from the rejection filter.

IPC 8 full level
H04N 5/50 (2006.01); **H03F 3/19** (2006.01); **H03H 7/01** (2006.01)

CPC (source: EP KR)
H03F 3/191 (2013.01 - EP); **H03H 5/02** (2013.01 - EP); **H04B 1/10** (2013.01 - EP); **H04B 1/18** (2013.01 - EP); **H04N 5/50** (2013.01 - KR); **H03F 2200/114** (2013.01 - EP); **H03F 2200/165** (2013.01 - EP); **H03F 2200/216** (2013.01 - EP); **H03F 2200/225** (2013.01 - EP); **H03F 2200/237** (2013.01 - EP); **H03F 2200/391** (2013.01 - EP); **H03F 2200/451** (2013.01 - EP); **H03F 2200/75** (2013.01 - EP)

Designated contracting state (EPC)
ES FR GB

Designated extension state (EPC)
AL BA HR MK RS

DOCDB simple family (publication)
WO 2008035893 A1 20080327; EP 2052537 A1 20090429; EP 2052537 A4 20110316; KR 20080026968 A 20080326

DOCDB simple family (application)
KR 2007004497 W 20070918; EP 07808287 A 20070918; KR 20060092262 A 20060922