

Title (en)

CONVERTING NON-EQUIDISTANT SIGNALS INTO EQUIDISTANT SIGNALS

Title (de)

UMSETZEN VON NICHTÄQUIDISTANTEN SIGNALEN IN ÄQUIDISTANTE SIGNALE

Title (fr)

CONVERSION DE SIGNAUX NON ÉQUIDISTANTS EN SIGNAUX ÉQUIDISTANTS

Publication

EP 2055005 A1 20090506 (EN)

Application

EP 06778339 A 20060824

Priority

EP 2006065616 W 20060824

Abstract (en)

[origin: WO2008022653A1] A signal processing device (100), preferably an undersampling ADC using coherent sampling, is presented for processing a signal (102) , the signal processing device (100) comprising a comparator unit (105) for comparing the signal (102) with a reference signal (106), a generation unit (105, 130) for generating digital result signals (110) indicative of the result of the comparing, an evaluation unit (112) for determining transition times of the digital result signals (110), and an output signal calculation unit (115) adapted for calculating essentially uniformly spaced output signals (116).

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

See references of WO 2008022653A1

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Designated extension state (EPC)

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DOCDB simple family (application)

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