

Title (en)

ANALOG-TO-DIGITAL CONVERSION USING ASYNCHRONOUS CURRENT-MODE CYCLIC COMPARISON

Title (de)

ANALOG-DIGITAL-UMSETZUNG UNTER VERWENDUNG EINES ASYNCHRONEN ZYKLISCHEN STROMMODUS-VERGLEICHS

Title (fr)

CONVERTISSEUR ANALOGIQUE/NUMÉRIQUE À COMPARAISON DE COURANT CYCLIQUE ASYNCHRONE

Publication

EP 2055006 A4 20100602 (EN)

Application

EP 07764097 A 20070720

Priority

- CN 2007002213 W 20070720
- US 49224506 A 20060725

Abstract (en)

[origin: US2008024346A1] An asynchronous cyclic current-mode analog-to-digital converter (ADC) is disclosed. The ADC comprises a plurality of sub-ADCs cascading from the first stage to the last stage, each sub-ADC comprising a current-mode ADC having a digital output, an analog current input, a reference current input and an analog current output. The analog current input of each stage, except the first stage, is operatively connected to the analog current output of the immediately preceding stage. The plurality of sub-ADCs are configured to operate without synchronization with each other.

IPC 8 full level

H03M 1/44 (2006.01)

CPC (source: EP KR US)

H03M 1/125 (2013.01 - EP US); **H03M 1/44** (2013.01 - KR); **H03M 1/447** (2013.01 - EP US)

Citation (search report)

- No further relevant documents disclosed
- See references of WO 2008014676A1

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

US 2008024346 A1 20080131; **US 7414562 B2 20080819**; AT E553540 T1 20120415; EP 2055006 A1 20090506; EP 2055006 A4 20100602; EP 2055006 B1 20120411; JP 2009545215 A 20091217; JP 4897047 B2 20120314; KR 101020672 B1 201110309; KR 20090034908 A 20090408; WO 2008014676 A1 20080207

DOCDB simple family (application)

US 49224506 A 20060725; AT 07764097 T 20070720; CN 2007002213 W 20070720; EP 07764097 A 20070720; JP 2009521093 A 20070720; KR 20097001472 A 20070720