

Title (en)

VALIDATION PROCESS FOR FAULT DETECTION OF A DEVICE

Title (de)

VALIDIERUNGSVERFAHREN ZUR FEHLERDETEKTION EINER VORRICHTUNG

Title (fr)

PROCÉDÉ DE VALIDATION D'UN DIAGNOSTIC DE FONCTIONNEMENT D'UN DISPOSITIF

Publication

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Application

**EP 07803972 A 20070629**

Priority

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- FR 0606472 A 20060717

Abstract (en)

[origin: WO2008009835A2] The invention concerns a process and an associated device for validating the fault detection of a device under test, during which a series of basic tests of the device under test is carried out, wherein each basic test leads to a positive or negative result indicating normal or abnormal functioning of the device under test. The process according to the invention also comprises the following steps: after each realization of a basic test, updated by an event counter (CRT) and updated by a result counter (FCRT), validation is performed depending on test result once the event counter has reached a first maximal value (CTRmax) or once the realization counter has reached a second maximal value (FCTRmax). Application in monitoring of the electric or functional performance of more or less complex systems installed in an automobile.

IPC 8 full level

**G01M 99/00** (2011.01); **G06F 11/273** (2006.01)

CPC (source: EP US)

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