

Title (en)

VALIDATION PROCESS FOR FAULT DETECTION OF A DEVICE

Title (de)

VALIDIERUNGSVERFAHREN ZUR FEHLERDETEKTION EINER VORRICHTUNG

Title (fr)

PROCÉDÉ DE VALIDATION D'UN DIAGNOSTIC DE FONCTIONNEMENT D'UN DISPOSITIF

Publication

EP 2084609 A2 20090805 (FR)

Application

EP 07803972 A 20070629

Priority

- FR 2007051564 W 20070629
- FR 0606472 A 20060717

Abstract (en)

[origin: WO2008009835A2] The invention concerns a process and an associated device for validating the fault detection of a device under test, during which a series of basic tests of the device under test is carried out, wherein each basic test leads to a positive or negative result indicating normal or abnormal functioning of the device under test. The process according to the invention also comprises the following steps: after each realization of a basic test, updated by an event counter (CRT) and updated by a result counter (FCRT), validation is performed depending on test result once the event counter has reached a first maximal value (CTRmax) or once the realization counter has reached a second maximal value (FCTRmax). Application in monitoring of the electric or functional performance of more or less complex systems installed in an automobile.

IPC 8 full level

G01M 99/00 (2011.01); **G06F 11/273** (2006.01)

CPC (source: EP US)

G06F 11/273 (2013.01 - EP US)

Citation (search report)

See references of WO 2008009835A2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

FR 2903774 A1 20080118; FR 2903774 B1 20080905; EP 2084609 A2 20090805; JP 2009545725 A 20091224; US 2010049473 A1 20100225; US 8650003 B2 20140211; WO 2008009835 A2 20080124; WO 2008009835 A3 20090723

DOCDB simple family (application)

FR 0606472 A 20060717; EP 07803972 A 20070629; FR 2007051564 W 20070629; JP 2009520018 A 20070629; US 37396107 A 20070629