

Title (en)  
SIGMA-DELTA MODULATOR

Title (de)  
SIGMA-DELTA-MODULATOR

Title (fr)  
MODULATEUR SIGMA-DELTA

Publication  
**EP 2084816 A1 20090805 (FR)**

Application  
**EP 07821928 A 20071026**

Priority  
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Abstract (en)  
[origin: FR2907988A1] The modulator has an analog to digital converter (10) placed in a closed control loop and providing an output digital signal (s). The signal is subtracted from an input analog signal (e) after conversion by a digital to analog converter (11). The converter (11) is placed in a loop between an output of the modulator and an input of a summer (12). The difference between the signals is filtered and amplified. Non decided bits at an output of the converter (10) are assigned with same values in the output digital signal and in a digital signal that is re-sent towards an input of the modulator.

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CPC (source: EP US)  
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