

Title (en)

METHOD AND CIRCUIT FOR RECEIVING DATA

Title (de)

VERFAHREN UND SCHALTUNG ZUM EMPFANGEN VON DATEN

Title (fr)

PROCÉDÉ ET CIRCUIT PERMETTANT DE RECEVOIR DES DONNÉES

Publication

EP 2092681 A1 20090826 (EN)

Application

EP 07849274 A 20071128

Priority

- IB 2007054816 W 20071128
- EP 06125048 A 20061129
- EP 07849274 A 20071128

Abstract (en)

[origin: WO2008065615A1] The invention relates to a circuit and method for receiving a signal of which - at the receiver end - the frequency is basically unknown. By sampling the data and deriving the frequency of the signal (or actually: the data rate of the data carried by the signal) and setting a phase locked loop in the receiver to the derived - estimated - circuit, the receiver can very quickly tune in to the frequency of the signal. Hence, no embedded or accompanying clock is required for the signal. Oversampling of the signal by the receiver front end is preferred, though.

IPC 8 full level

H04L 7/033 (2006.01); **H03L 7/08** (2006.01); **H04L 7/10** (2006.01)

CPC (source: EP US)

H03L 7/0991 (2013.01 - EP US); **H04L 7/0331** (2013.01 - EP US); **H04L 7/10** (2013.01 - EP US); **H03L 2207/50** (2013.01 - EP US)

Citation (search report)

See references of WO 2008065615A1

Cited by

CN104597802A

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC MT NL PL PT RO SE SI SK TR

DOCDB simple family (publication)

WO 2008065615 A1 20080605; AT E505870 T1 20110415; CN 101542959 A 20090923; CN 101542959 B 20121205; DE 602007013934 D1 20110526; EP 2092681 A1 20090826; EP 2092681 B1 20110413; US 2010172457 A1 20100708; US 8433000 B2 20130430

DOCDB simple family (application)

IB 2007054816 W 20071128; AT 07849274 T 20071128; CN 200780044133 A 20071128; DE 602007013934 T 20071128; EP 07849274 A 20071128; US 51678207 A 20071128