

Title (en)

RECONFIGURABLE POWER AMPLIFIER AND USE OF SUCH AMPLIFIER FOR MAKING A MULTISTANDARD AMPLIFICATION STAGE FOR MOBILE PHONE COMMUNICATIONS

Title (de)

REKONFIGURIERBARER LEISTUNGSVERSTÄRKER UND VERWENDUNG EINES SOLCHEN VERSTÄRKERS ZUR HERSTELLUNG EINES MULTISTANDARD-VERSTÄRKUNGSZUSTANDES FÜR MOBILTELEFONKOMMUNIKATIONEN

Title (fr)

AMPLIFICATEUR DE PUISSANCE RECONFIGURABLE ET UTILISATION D'UN TEL AMPLIFICATEUR POUR LA REALISATION D'UN ETAGE D'AMPLIFICATION MULTISTANDARD POUR LA TELEPHONIE MOBILE

Publication

**EP 2102981 A2 20090923 (FR)**

Application

**EP 08761931 A 20080114**

Priority

- FR 2008050059 W 20080114
- FR 0752699 A 20070116

Abstract (en)

[origin: FR2911447A1] The amplifier has a CPU for controlling a pilotage stage and a power stage based on a high frequency input signal (RFin), which is applied to the CPU. The CPU has bias circuits (4, 5) for modifying compression points of the stages, where the bias circuits adapt to gain of the stages to increase a power added efficiency of the stages for the modified compression points. The bias circuits respectively adapt to function of the stages. Switches (7, 8) selectively connect a capacitor (C') and an inductor (L') that are mounted in parallel to an output of the power stage.

IPC 8 full level

**H03F 1/02** (2006.01); **H03F 1/56** (2006.01); **H03F 3/19** (2006.01); **H03F 3/68** (2006.01)

CPC (source: EP US)

**H03F 1/0266** (2013.01 - EP US); **H03F 1/565** (2013.01 - EP US); **H03F 3/19** (2013.01 - EP US); **H03F 3/217** (2013.01 - EP US);  
**H03F 3/68** (2013.01 - EP US)

Citation (search report)

See references of WO 2008099113A2

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US 2010109770 A1 20100506; US 8115547 B2 20120214; WO 2008099113 A2 20080821; WO 2008099113 A3 20090115

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