

Title (en)

SECURED ELECTRONIC SYSTEMS, SECURING METHODS AND USES OF SUCH SYSTEMS

Title (de)

GESICHERTE ELEKTRONISCHE SYSTEME, SICHERUNGSVERFAHREN UND VERWENDUNG SOLCHER SYSTEME

Title (fr)

SYSTEMES ELECTRONIQUES SECURISES, PROCEDES DE SECURISATION ET UTILISATIONS DE TELS SYSTEMES

Publication

EP 2104893 A2 20090930 (FR)

Application

EP 07872435 A 20071221

Priority

- FR 2007002152 W 20071221
- FR 0611283 A 20061222

Abstract (en)

[origin: FR2910658A1] The system has a physical address allocating module for writing data in a persistent non-volatile memory e.g. electrically EPROM. The allocating module allocates the addresses in the memory by a variant e.g. chip identifier, according to behavioral factors such as life cycle of the system and external information transmitted by the system. A memory management module is provided for managing the data stored in the memory. The management module is connected with the allocating module. An independent claim is also included for a method for securing an electronic system that is provided with a micro-processor and a data storing memory.

IPC 8 full level

G06F 12/14 (2006.01); **G06F 21/72** (2013.01); **G06F 21/77** (2013.01); **G06F 21/79** (2013.01)

CPC (source: EP)

G06F 12/1408 (2013.01); **G06F 21/72** (2013.01); **G06F 21/77** (2013.01); **G06F 21/79** (2013.01)

Citation (search report)

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Designated contracting state (EPC)

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DOCDB simple family (publication)

FR 2910658 A1 20080627; **FR 2910658 B1 20090220**; BR PI0721042 A2 20140729; EP 2104893 A2 20090930; JP 2010514039 A 20100430; WO 2008096076 A2 20080814; WO 2008096076 A3 20081002

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FR 0611283 A 20061222; BR PI0721042 A 20071221; EP 07872435 A 20071221; FR 2007002152 W 20071221; JP 2009542142 A 20071221