

Title (en)
IMAGE DISPLAY DEVICE

Title (de)
BILDANZEIGEVORRICHTUNG

Title (fr)
DISPOSITIF D'AFFICHAGE D'IMAGE

Publication
EP 2116989 A4 20100825 (EN)

Application
EP 08720307 A 20080229

Priority
• JP 2008000393 W 20080229
• JP 2007050995 A 20070301

Abstract (en)
[origin: EP2116989A1] An image display device includes an error diffusion circuit for limiting the gradation level of an image signal to that displayable on the image display device, and diffusing error data produced by the limitation to neighboring pixels. The error diffusion circuit includes an error replacement unit for replacing the error data with fixed-value data in a predetermined period of the image signal.

IPC 8 full level
G09G 3/20 (2006.01); **G09G 3/28** (2013.01); **G09G 3/288** (2013.01); **G09G 3/291** (2013.01); **G09G 3/292** (2013.01); **G09G 3/293** (2013.01);
G09G 3/294 (2013.01); **G09G 3/296** (2013.01); **G09G 3/298** (2013.01)

CPC (source: EP KR US)
G09G 3/2059 (2013.01 - EP US); **G09G 3/2066** (2013.01 - EP US); **G09G 3/288** (2013.01 - KR); **G09G 2310/0232** (2013.01 - EP US)

Citation (search report)
• [IP] US 2007230813 A1 20071004 - YAMAMOTO AKIRA [JP], et al
• See references of WO 2008108075A1

Designated contracting state (EPC)
DE FR GB NL

DOCDB simple family (publication)
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JP WO2008108075 A1 20100610; KR 101002510 B1 20101217; KR 20090086223 A 20090811; US 2010033509 A1 20100211;
WO 2008108075 A1 20080912

DOCDB simple family (application)
EP 08720307 A 20080229; CN 200880001390 A 20080229; JP 2008000393 W 20080229; JP 2008539178 A 20080229;
KR 20097010426 A 20080229; US 44662608 A 20080229