

Title (en)

SYSTEM AND METHOD OF OPERATING MEMORY DEVICES OF MIXED TYPE

Title (de)

SYSTEM UND VERFAHREN ZUM BETRIEB VON SPEICHERANORDNUNGEN DES GEMISCHTEN TYP

Title (fr)

SYSTEME ET PROCEDE DE FONCTIONNEMENT DE DISPOSITIFS DE MEMOIRE DE TYPES COMBINES

Publication

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Application

EP 07855464 A 20071204

Priority

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- US 86877306 P 20061206
- US 87089206 P 20061220
- US 62282807 A 20070112
- US 77124107 A 20070629

Abstract (en)

[origin: WO2008067658A1] A memory system architecture is provided in which a memory controller controls memory devices in a serial interconnection configuration. The memory controller has an output port for sending memory commands and an input port for receiving memory responses for those memory commands requisitioning such responses. Each memory device includes a memory, such as, for example, NAND-type flash memory, NOR-type flash memory, random access memory and static random access memory. Each memory command is specific to the memory type of a target memory device. A data path for the memory commands and the memory responses is provided by the interconnection. A given memory command traverses memory devices in order to reach its intended memory device of the serial interconnection configuration. Upon its receipt, the intended memory device executes the given memory command and, if appropriate, sends a memory response to a next memory device. The memory response is transferred to the memory controller.

IPC 8 full level

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CPC (source: EP KR)

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Citation (search report)

- [I] US 2006031593 A1 20060209 - SINCLAIR ALAN W [GB]
- [I] US 2004256638 A1 20041223 - PEREGO RICHARD [US], et al
- [A] US 2005120136 A1 20050602 - PARK YOUNG-JUN [KR], et al
- See references of WO 2008067658A1

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