

Title (en)

POWER AMPLIFIER TIME-DELAY INVARIANT PREDISTORTION METHODS AND APPARATUS

Title (de)

ZEITINVARIANTES VORVERZERRUNGSVERFAHREN UND VORRICHTUNG FÜR LEISTUNGSVERSTÄRKER

Title (fr)

PROCÉDÉS ET APPAREIL DE PRÉDISTORSION INDÉPENDANTE DU TEMPS DE PROPAGATION D'UN AMPLIFICATEUR DE PUISSANCE

Publication

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Application

EP 08806836 A 20080128

Priority

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- US 89774607 P 20070126
- US 89831207 P 20070129

Abstract (en)

[origin: WO2008155610A2] An embodiment of the invention is a time-delay invariant predistortion approach to linearize power amplifiers in wireless RF transmitters. The predistortion architecture is based on the stored-compensation or memory-compensation principle by using a combined time-delay addressing method, and therefore, the architecture has an intrinsic, self-calibrating time-delay compensation function. The predistortion architecture only uses a lookup table to conduct both the correction of non-linear responses of a power amplifier and the compensation of any time-delay effects presented in the same system. Due to the time-delay invariant characteristic, the predistortion design has a wider dynamic range processing advantage for wireless RF signals, and therefore can be implemented in multi-carrier and multi-channel wireless systems.

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

- [XD] US 2006046665 A1 20060302 - YANG DALI [US], et al
- [XI] US 6998909 B1 20060214 - MAUER VOLKER [GB]
- See references of WO 2008155610A2

Designated contracting state (EPC)

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