

Title (en)
Viewing device with secured matrix screen

Title (de)
Anzeigevorrichtung mit Hilfe eines gesicherten Matrixbildschirms

Title (fr)
Dispositif de visualisation à écran matriciel sécurisé

Publication
EP 2151810 A3 20100623 (FR)

Application
EP 09165124 A 20090710

Priority
FR 0804538 A 20080808

Abstract (en)
[origin: EP2151810A2] The device has a LCD face-plate comprising a backplane electrode (E.B.), and a matrix of electrodes controlled by a line driver's electronic card (C.D.L.). An analog quenching circuit applies a voltage (V_{backplane}) to the backplane electrode for uniformly turning on all the transistors to cause erasure of an image on a thin film transistor (TFT) screen, when an electronic signal is representative of incorrect operation of the card, so that the screen is uniformly monochrome. A value of the backplane electrode voltage is equal to a reduced value (V_{g-off}) of a threshold voltage (V_{seuil}).

IPC 8 full level
G09G 3/20 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP US)
G09G 3/006 (2013.01 - EP US); **G09G 3/3655** (2013.01 - EP US); **G09G 3/20** (2013.01 - EP US); **G09G 2310/0245** (2013.01 - EP US)

Citation (search report)

- [X] KR 20020094637 A 20021218 - SAMSUNG ELECTRONICS CO LTD [KR]
- [A] US 2007222474 A1 20070927 - DE LAUZUN FREDERIC [FR]
- [A] US 2006290645 A1 20061228 - JEON DONG H [KR]
- [A] JP 2007286316 A 20071101 - SEIKO EPSON CORP
- [A] FR 2843823 A1 20040227 - THALES SA [FR]

Cited by
FR2984578A1; CN105574906A; US9709833B2

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK SM TR

Designated extension state (EPC)
AL BA RS

DOCDB simple family (publication)
EP 2151810 A2 20100210; EP 2151810 A3 20100623; FR 2934917 A1 20100212; FR 2934917 B1 20101210; US 2010033465 A1 20100211

DOCDB simple family (application)
EP 09165124 A 20090710; FR 0804538 A 20080808; US 51040709 A 20090728