

Title (en)

WATCHDOG MECHANISM WITH FAULT RECOVERY

Title (de)

WATCHDOG-MECHANISMUS MIT FEHLERBEHEBUNG

Title (fr)

MÉCANISME DE SURVEILLANCE AVEC RÉCUPÉRATION DES PANNES

Publication

EP 2176757 A2 20100421 (EN)

Application

EP 08786592 A 20080729

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- DE 102007035584 A 20070730
- US 1675107 P 20071226
- US 1675207 P 20071226

Abstract (en)

[origin: WO2009016187A2] A method for handling watchdog events of an electronic device includes detecting a watchdog fault in a normal mode, which is a watchdog event in which a watchdog trigger is not correctly serviced; entering from the normal mode into a first escalation level of nx escalation levels upon detection of the watchdog fault, wherein nx is an integer equal to or greater than 1; detecting correct watchdog events, which are watchdog events in which a watchdog trigger is correctly serviced; and concurrently detecting watchdog faults, leaving the first escalation level if a first escalation condition is met, and recovering in a recovering step back from any of the nx escalation levels to a previous level or mode, if a de-escalation condition is met. An electronic device embodiment includes a CPU and program instructions for carrying out the method.

IPC 8 full level

G06F 11/07 (2006.01)

CPC (source: EP)

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Citation (search report)

See references of WO 2009016187A2

Citation (examination)

WO 2006014400 A1 20060209 - INTEL CORP [US], et al

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Designated extension state (EPC)

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