

Title (en)

CIRCUIT ARRANGEMENT FOR SIGNAL RECEPTION AND GENERATION AND METHOD FOR OPERATING SAID CIRCUIT ARRANGEMENT

Title (de)

SCHALTUNGSANORDNUNG ZUR SIGNALAUFNAHME UND -ERZEUGUNG SOWIE VERFAHREN ZUM BETREIBEN DIESER SCHALTUNGSANORDNUNG

Title (fr)

CIRCUIT POUR ENREGISTRER ET PRODUIRE DES SIGNAUX ET PROCÉDÉ D'EXPLOITATION DUDIT CIRCUIT

Publication

**EP 2193416 A2 20100609 (DE)**

Application

**EP 08802992 A 20080808**

Priority

- EP 2008060493 W 20080808
- DE 102007044803 A 20070920

Abstract (en)

[origin: WO2009040179A2] The invention relates to a circuit arrangement (CA) for signal reception and generation, and to a method for operating said circuit arrangement. The circuit comprises at least one timer module (TM) for providing a time basis to a plurality of time control modules (TC1... TCx) connected thereto, and a time routing unit (TRU) connected to the modules for interconnecting said modules (TM, TC1... TCx) and the signals thereof.

IPC 8 full level

**G06F 1/04** (2006.01); **G06F 9/46** (2006.01); **G06F 13/16** (2006.01)

CPC (source: EP US)

**G06F 1/04** (2013.01 - EP US)

Citation (search report)

See references of WO 2009040179A2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT RO SE SI SK TR

Designated extension state (EPC)

AL BA MK RS

DOCDB simple family (publication)

**WO 2009040179 A2 20090402; WO 2009040179 A3 20090625**; CN 101802746 A 20100811; CN 101802746 B 20131127; DE 102007044803 A1 20090409; EP 2193416 A2 20100609; JP 2010539615 A 20101216; JP 5130369 B2 20130130; US 2010195670 A1 20100805; US 8750294 B2 20140610

DOCDB simple family (application)

**EP 2008060493 W 20080808**; CN 200880108227 A 20080808; DE 102007044803 A 20070920; EP 08802992 A 20080808; JP 2010525285 A 20080808; US 66969208 A 20080808