Title (en)
VERIFICATION OF DATA READ IN MEMORY

Title (de)
VERIFIZIERUNG VON IN EINEM SPEICHER GELESENEN DATEN

Title (fr)
VERIFICATION DE DONNEES LUES EN MEMOIRE

Publication
EP 2212824 A2 20100804 (FR)

Application
EP 08857341 A 20081118

Priority
• FR 2008052073 W 20081118
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Abstract (en)
[origin: WO2009071791A2] The invention relates to a method and a circuit for verifying data transferred between a circuit (21) and a processing unit (11), in which: the data originating from the circuit travels through a first temporary storage element (23) having a size representing an integer multiple of the size of data liable to be presented subsequently on a bus (27) of the processing unit; an address provided by the processing unit (11) destined for the circuit is stored temporarily in a second element (22); and the content of the first element is compared with a current data item (CDATA) originating from the circuit, at least when said data item corresponds to an address of a data item already present in this first element.

IPC 8 full level
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