Title (en)
ON DATA PROCESSING

Title (de)
COMPILER FÜR REKONFIGURIERBARE ARCHITEKTUREN MIT BESONDEREM ZWISCHENFORMAT

Title (fr)
TRAITEMENT DE L'INFORMATION

Publication
EP 2217999 A2 20100818 (DE)

Application
EP 08855171 A 20081128

Priority
• DE 2008001971 W 20081128
• DE 102007057642 A 20071128

Abstract (en)
[origin: WO2009068014A2] The invention relates to a method for compiling high-level language code for various architectures and/or components. The invention proposes that an architecture-specific precompilation be generated and subsequently the architecture-specific precompilation be compiled taking into account component-specific information.

IPC 8 full level
G06F 9/45 (2006.01)

CPC (source: EP US)
G06F 8/447 (2013.01 - EP US); **G06F 8/47** (2013.01 - EP US); **G06F 9/45516** (2013.01 - EP US)

Citation (search report)
See references of WO 2009068014A2

Citation (examination)
PIOTR LESNICKI ET AL: "Split Compilation: an Application to Just-in-Time Vectorization", INTERNATIONAL WORKSHOP ON GCC FOR RESEARCH IN EMBEDDED AND PARALLEL SYSTEMS, GREPS 2007, 16 September 2007 (2007-09-16), pages 1 - 7, XP055171902, Retrieved from the Internet <URL:http://citeseerx.ist.psu.edu/viewdoc/download?doi=10.1.1.73.3450&rep=rep1&type=pdf> [retrieved on 20150225]

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT RO SE SI SK TR

Designated extension state (EPC)
AL BA MK RS

DOCDB simple family (publication)
WO 2009068014 A2 20090604; **WO 2009068014 A3 20100805**; DE 112008003670 A5 20101028; EP 2217999 A2 20100818; US 2011173596 A1 20110714

DOCDB simple family (application)
DE 2008001971 W 20081128; DE 112008003670 T 20081128; EP 08855171 A 20081128; US 74533508 A 20081128