

Title (en)

Pixel circuit, display using the same and driving method for the same

Title (de)

Pixelerschaltung, Anzeigevorrichtung damit und Ansteuerungsverfahren dafür

Title (fr)

Circuit de pixels, appareil d'affichage l'utilisant et son procédé de commande

Publication

EP 2237253 A1 20101006 (EN)

Application

EP 09157123 A 20090401

Priority

EP 09157123 A 20090401

Abstract (en)

A pixel circuit including a switching transistor (210) for receiving a control signal and a data voltage, a driving transistor (202), connected to the switching transistor, a first and a second power supply voltage line, a capacitor (204), a current-controlled light emitting means (206), which is connected between the driving transistor and a second power supply voltage line, adapted to generate light so as to display images. The pixel circuit further comprises a threshold voltage variation cancellation circuit (20) having two terminals, the first terminal whereof is connected to said first power supply voltage line means, and the second terminal is connected to the source terminal of the said driving transistor means, providing an output voltage value to said driving transistor means which is such that a predetermined current is supplied to the light emitting means.

IPC 8 full level

G09G 3/32 (2006.01)

CPC (source: EP)

G09G 3/3233 (2013.01); **G09G 2300/0819** (2013.01); **G09G 2300/0833** (2013.01); **G09G 2300/0842** (2013.01); **G09G 2300/0871** (2013.01); **G09G 2320/043** (2013.01)

Citation (applicant)

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Citation (search report)

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Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK TR

Designated extension state (EPC)

RS

DOCDB simple family (publication)

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DOCDB simple family (application)

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