

Title (en)

PLD architecture for flexible placement of IP function blocks

Title (de)

Architektur für programmierbare logische Vorrichtung zum flexiblen Plazieren von Funktionsblöcken geistigen Eigentums

Title (fr)

Architecture de réseau logique programmable pour le placement flexible de blocs de fonction de propriété intellectuelle

Publication

**EP 2244384 A3 20101124 (EN)**

Application

**EP 10004986 A 20020410**

Priority

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- US 5744202 A 20020125

Abstract (en)

[origin: US2002163356A1] In accordance with one aspect of the invention, a hole is formed within an LE array of a PLD by interrupting the LE array base signal routing architecture such that a hole is left for IP function block to be incorporated. An interface region is provided for interfacing the remaining LE array base signal routing architecture to the IP function block.

IPC 8 full level

**H01L 21/82** (2006.01); **H03K 19/173** (2006.01); **H03K 19/177** (2006.01)

CPC (source: EP US)

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Citation (search report)

- [X] WO 9956394 A1 19991104 - ACTEL CORP [US]
- [A] "MIXTURE OF FIELD AND FACTORY PROGRAMMED LOGIC CELLS IN A SINGLE DEVICE", IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 38, no. 4, 1 April 1995 (1995-04-01), pages 499, XP000516233, ISSN: 0018-8689

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