

Title (en)

PHASE SYNCHRONIZATION DEVICE AND PHASE SYNCHRONIZATION METHOD

Title (de)

PHASENSYNCHRONISATIONSEINRICHTUNG UND PHASENSYNCHRONISATIONSVERFAHREN

Title (fr)

DISPOSITIF DE SYNCHRONISATION DE PHASE ET PROCÉDÉ DE SYNCHRONISATION DE PHASE

Publication

**EP 2249534 A1 20101110 (EN)**

Application

**EP 09707566 A 20090129**

Priority

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- JP 2008023779 A 20080204

Abstract (en)

A sampling section (100A) includes a sampling filter (102) that converts a continuous-time signal into a discrete-time signal and applies filtering of low-pass characteristics and a one-bit quantizer (107) that outputs a quantized signal representing a time-dependent change in the discrete-time signal. A synchronization section (100B) includes a phase difference detector (110) that calculates the phase difference between an inspection signal and the quantized signal and a delay control circuit (114) that feeds back the inspection signal to the phase difference detector at the timing set in consideration of a delay amount corresponding to the phase difference. When the phase difference between the inspection signal and the current quantized signal shows the same phase, the phase of the inspection signal is detected as a reference phase.

IPC 8 full level

**H04L 27/227** (2006.01); **H04L 7/033** (2006.01)

CPC (source: EP US)

**H04L 7/0337** (2013.01 - EP US); **H04L 27/0014** (2013.01 - EP US); **H04L 27/227** (2013.01 - EP US); **H04L 2027/0053** (2013.01 - EP US); **H04L 2027/0067** (2013.01 - EP US)

Designated contracting state (EPC)

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DOCDB simple family (publication)

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