

Title (en)

OPTIMIZATION OF INTEGRATED CIRCUIT DESIGN AND LIBRARY

Title (de)

OPTIMIERUNG DES ENTWURFS UND DER BIBLIOTHEK INTEGRIERTER SCHALTUNGEN

Title (fr)

OPTIMISATION DE LA CONCEPTION D'UN CIRCUIT INTÉGRÉ ET D'UNE BIBLIOTHÈQUE

Publication

EP 2257900 A4 20121017 (EN)

Application

EP 09709144 A 20090205

Priority

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- US 2622208 P 20080205

Abstract (en)

[origin: WO2009100237A2] A method co-optimizes a design and a library in such a way to choose the best set of cells to implement the design. The method takes into account the idea of limiting the number of new cells while reducing target costs and respecting design constraints. The method chooses a minimum nearly optimum set of cells to optimize a design. This involves the simultaneous optimization of a cell-based design and a cell library used to implement it. The invention can produce only an optimized library for a specific application, when the circuit is disregarded. The method takes into account a set of new cells described as finalized cells or as virtual cells, possibly having different transistor topologies, different sizes, different logic functions, and/or different cell template than the original library.

IPC 8 full level

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CPC (source: EP)

G06F 30/327 (2020.01); **G06F 2119/06** (2020.01); **G06F 2119/12** (2020.01)

Citation (search report)

- [X] US 2001032067 A1 20011018 - NEMANI MAHADEVAMURTY [US], et al
- [X] US 2001010090 A1 20010726 - BOYLE DOUGLAS B [US], et al
- See references of WO 2009100237A2

Designated contracting state (EPC)

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DOCDB simple family (publication)

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DOCDB simple family (application)

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