

Title (en)
METHOD FOR ADDRESSING PAGE-ORIENTED NON-VOLATILE MEMORIES

Title (de)
VERFAHREN ZUM ADRESSIEREN VON SEITENORIENTIERTEN NICHTFLÜCHTIGEN SPEICHERN

Title (fr)
PROCÉDÉ POUR ADRESSER DES MÉMOIRES NON VOLATILES À PAGINATION

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Application
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Abstract (en)
[origin: WO2009143885A1] The invention describes a method for addressing memory pages of a non-volatile memory in a memory system (MS) with a memory controller (MC) and a further volatile memory (RAM), wherein the non-volatile memory is organized in erasable memory blocks with a multiplicity of memory pages, and each memory page, containing a number of sectors, can be written individually, and wherein the volatile memory holds an address translation table (AT) specifying an assignment of logical memory page addresses (LPA) to physical memory page addresses (PPA), wherein by means of the memory controller (MC), a reconstruction table (RT) is stored as a copy of the address translation table in one or more memory blocks in the non-volatile memory (FM), a log book table (LBK) with data records containing changed assignments of logical memory page addresses (LPA) to physical memory page addresses (PPA) is carried in the volatile memory (RAM) and, if the log book table (LBK) exceeds a predetermined size, a changed reconstruction table (RT) is stored in the non-volatile memory.

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