

Title (en)

System with dual rail regulated locked loop

Title (de)

System mit zweigleisiger geregelter Phasenregelschleife

Title (fr)

Système à boucle DLL régulée sur rail double

Publication

**EP 2296276 A3 20121107 (EN)**

Application

**EP 10183041 A 20030320**

Priority

- EP 03716786 A 20030320
- US 10423002 A 20020322
- US 40806302 P 20020903
- US 40810802 P 20020903
- US 43675602 P 20021227
- US 37425203 A 20030225

Abstract (en)

[origin: US2003179028A1] An integrated circuit device having a select circuit, a summing circuit and a phase mixer. The select circuit selects one of a plurality of offset values as a selected offset. The summing circuit sums the selected offset with a phase count value, the phase count value indicating a phase difference between a reference clock signal and one of a first plurality of clock signals. The phase mixer combines the first plurality of clock signals in accordance with the sum of the selected offset and the phase count value to generate an output clock signal.

IPC 8 full level

**H03L 7/00** (2006.01); **G06F 1/10** (2006.01); **H03K 5/13** (2006.01); **H03L 7/07** (2006.01); **H03L 7/081** (2006.01)

CPC (source: EP US)

**G06F 1/10** (2013.01 - EP US); **H03K 5/135** (2013.01 - EP US); **H03L 7/07** (2013.01 - EP US); **H03L 7/0805** (2013.01 - EP US); **H03L 7/0814** (2013.01 - EP US); **H03L 7/0816** (2013.01 - EP US); **H03K 2005/00052** (2013.01 - EP US)

Citation (search report)

- [XY] US 5039893 A 19910813 - TOMISAWA NORIO [JP]
- [Y] US 5231319 A 19930727 - CRAFTS HAROLD S [US], et al
- [Y] US 6339354 B1 20020115 - HEIGHTLEY JOHN [US]
- [Y] US 5684421 A 19971104 - CHAPMAN DOUGLAS J [US], et al
- [Y] US 4072910 A 19780207 - DINGWALL ANDREW GORDON FRANCIS, et al

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EP2610704A3; US9219410B2; US9513649B2; TWI499184B

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PT RO SE SI SK TR

DOCDB simple family (publication)

**US 2003179028 A1 20030925; US 6952123 B2 20051004;** AU 2003220477 A1 20031013; EP 1497923 A1 20050119; EP 2296276 A2 20110316; EP 2296276 A3 20121107; EP 2296277 A2 20110316; EP 2296277 A3 20121212; EP 2296278 A2 20110316; EP 2296278 A3 20120523; US 2005189971 A1 20050901; US 7046056 B2 20060516; WO 03084067 A2 20031009

DOCDB simple family (application)

**US 37425203 A 20030225;** AU 2003220477 A 20030320; EP 03716786 A 20030320; EP 10183041 A 20030320; EP 10183089 A 20030320; EP 10183128 A 20030320; US 0308873 W 20030320; US 11443305 A 20050426