

Title (en)

D-CLASS DIGITAL AMPLIFIER INCLUDING A NOISE REDUCER

Title (de)

KLASSE-D-DIGITALVERSTÄRKER MIT EINEM RAUSCHMINDERER

Title (fr)

AMPLIFICATEUR NUMÉRIQUE CLASSE D COMPRENANT UN REDUCTEUR DE BRUIT

Publication

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Application

EP 09766012 A 20090616

Priority

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Abstract (en)

[origin: WO2009153449A2] The invention relates to an amplifier (A2) including a digital delta-sigma modulator (2), a quantifier (QT1) receiving a signal (NSS) provided by a delta-sigma stage (DS5) and providing a quantified signal (QS), and a power circuit (PA) providing an output signal (OS). The device includes N state loops of a first type configured for sending the output signal (OS) back to adders of N lower-rank delta-sigma stages, each state loop of the first type including an analog low-pass filter (LPF1) for providing a filtered output signal (FOS), and an analog-to-digital converter (ADC) for providing a digitised filtered output signal (DFOS).

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

See references of WO 2009153450A2

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DOCDB simple family (publication)

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