

Title (en)

D-CLASS DIGITAL AMPLIFIER INCLUDING A NOISE REDUCER

Title (de)

KLASSE-D-DIGITALVERSTÄRKER MIT EINEM RAUSCHMINDERER

Title (fr)

AMPLIFICATEUR NUMERIQUE CLASSE D COMPRENANT UN REDUCTEUR DE BRUIT

Publication

**EP 2301147 A2 20110330 (FR)**

Application

**EP 09766012 A 20090616**

Priority

- FR 2009000716 W 20090616
- FR 0803350 A 20080616
- FR 0803348 A 20080616

Abstract (en)

[origin: WO2009153449A2] The invention relates to an amplifier (A2) including a digital delta-sigma modulator (2), a quantifier (QT1) receiving a signal (NSS) provided by a delta-sigma stage (DS5) and providing a quantified signal (QS), and a power circuit (PA) providing an output signal (OS). The device includes N state loops of a first type configured for sending the output signal (OS) back to adders of N lower-rank delta-sigma stages, each state loop of the first type including an analog low-pass filter (LPF1) for providing a filtered output signal (FOS), and an analog-to-digital converter (ADC) for providing a digitised filtered output signal (DFOS).

IPC 8 full level

**H03F 3/217** (2006.01)

CPC (source: EP US)

**H03F 3/2175** (2013.01 - EP US)

Citation (search report)

See references of WO 2009153450A2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO PL PT RO SE SI SK TR

Designated extension state (EPC)

AL BA RS

DOCDB simple family (publication)

**WO 2009153449 A2 20091223; WO 2009153449 A3 20100211**; EP 2301147 A2 20110330; EP 2308171 A2 20110413; US 2011148677 A1 20110623; US 8228222 B2 20120724; WO 2009153450 A2 20091223; WO 2009153450 A3 20100211

DOCDB simple family (application)

**FR 2009000715 W 20090616**; EP 09766011 A 20090616; EP 09766012 A 20090616; FR 2009000716 W 20090616; US 96944910 A 20101215