

Title (en)

Memory device comprising an array of nanoscale cells

Title (de)

Speichervorrichtung mit einem Array von nanoskaligen Zellen

Title (fr)

Dispositif de mémoire comprenant un réseau de cellules d'échelle nanométrique

Publication

EP 2309515 A1 20110413 (EN)

Application

EP 09172679 A 20091009

Priority

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Abstract (en)

The invention relates to a memory device (1) comprising an array of non-volatile memory cells (2). It also relates to a non-volatile memory chip comprising an array of such memory devices and capable to store both the digital and analogue values in such a memory device. The programming and reading methods and fabrication process are also presented. According to the invention, each memory cell (2) is embodied by a nanoscale component having three terminals, an electrical characteristic between a first and a second terminal of each nanoscale component (2) being able to represent a data and being able to be modified by applying a signal to this first terminal or to this second terminal, a third terminal of each nanoscale component being able to prevent the electrical characteristic of the corresponding nanoscale component from being modified and sensed by applying a positive and non-null voltage to it.

IPC 8 full level

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CPC (source: EP)

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Citation (applicant)

US 7323730 B2 20080129 - BORGHETTI JULIEN [FR], et al

Citation (search report)

- [X] US 2006250856 A1 20061109 - BERTIN CLAUDE L [US], et al
- [XY] V. DERYCKE ET AL.: "Dynamic Performances of Carbon nanotube Transistors and Programmable Devices for Adaptive Architectures", 11 September 2009 (2009-09-11), pages 1/25 - 25/25, XP002573396, Retrieved from the Internet <URL:http://tntconf.org/2009/Presentaciones/TNT2009_Derycke.pdf> [retrieved on 20100315]
- [Y] BORGHETTI J ET AL: "Optoelectronic Switch and Memory Devices Based on Polymer-Functionalized Carbon Nanotube Transistors", ANGEWANDTE CHEMIE. INTERNATIONAL EDITION, WILEY VCH VERLAG, WEINHEIM, no. 18, 1 January 2006 (2006-01-01), pages 2535 - 2540, XP002464501, ISSN: 1433-7851
- [A] G. AGNUS ET AL: "Carbon nanotube Programmable Devices For Adaptive Architectures", 11 September 2009 (2009-09-11), pages 1/2 - 2/2, XP002573474, Retrieved from the Internet <URL:http://www.tntconf.org/2009/Abstracts/Posters/TNT2009_Agnus.pdf?TNT=5ba318f4fccb64e7628a9cb7f38cb3be> [retrieved on 20100315]

Citation (examination)

KR 20080052250 A 20080611 - KOREA ELECTRONICS TELECOMM [KR], et al

Cited by

US9842991B2; US9165633B2

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