Title (en)

MEMORY SYSTEM AND METHOD USING STACKED MEMORY DEVICE DICE, AND SYSTEM USING THE MEMORY SYSTEM

Title (de)

SPEICHERSYSTEM UND VERFAHREN MIT GESTAPELTEN SPEICHERCHIPS UND SYSTEM MIT DEM SPEICHERSYSTEM

Title (fr)

SYSTÈME DE MÉMOIRE ET PROCÉDÉ UTILISANT DES PUCES DE DISPOSITIF DE MÉMOIRE EMPILÉES, ET SYSTÈME UTILISANT LE SYSTÈME DE MÉMOIRE

Publication

EP 2319044 B1 20140416 (EN)

Application

EP 09800787 A 20090709

Priority

- US 2009050155 W 20090709
- US 17695108 A 20080721

Abstract (en)

[origin: US2010014364A1] A memory system and method uses stacked memory device dice coupled to each other and to a logic die. The logic die may include a timing correction system that is operable to control the timing at which the logic die receives signals, such as read data signals, from each of the memory device dice. The timing correction controls the timing of the read data or other signals by adjusting the timing of respective strobe signals, such as read strobe signals, that are applied to each of the memory device dice. The memory device dice may transmit read data to the memory device at a time determined by when it receives the respective strobe signals. The timing of each of the strobe signals is adjusted so that the read data or other signals from all of the memory device dice are received at the same time.

IPC 8 full level

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