

Title (en)

SYSTEM AND METHOD FOR AUTOMATIC HARDWARE AND SOFTWARE SEQUENCING OF COMPUTER-AIDED DESIGN (CAD) FUNCTIONALITY TESTING

Title (de)

SYSTEM UND VERFAHREN FÜR AUTOMATISCHE HARDWARE- UND SOFTWARESEQUENZIERUNG EINES CAD-FUNKTIONSTESTS

Title (fr)

SYSTÈME ET PROCÉDÉ POUR SÉQUENCEMENT MATÉRIEL ET LOGICIEL AUTOMATIQUE DE TESTS FONCTIONNELS DE CONCEPTION ASSISTÉE PAR ORDINATEUR (CAO)

Publication

EP 2321751 A1 20110518 (EN)

Application

EP 09794099 A 20090707

Priority

- IL 2009000678 W 20090707
- US 12956608 P 20080707

Abstract (en)

[origin: WO2010004557A1] The present invention relates to a system and a method for creating hardware and/or software test sequences and in particular, to such a system and method in which modular building blocks are used to create, sequence and schedule a large scale testing sequence using a matrix like platform.

IPC 8 full level

G06F 11/263 (2006.01); **G06F 19/00** (2011.01)

CPC (source: EP US)

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Cited by

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